



# UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Adress: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
[www.uspto.gov](http://www.uspto.gov)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/764,803	01/26/2004	Payman Zarkesh-Ha	02-5938	9749
86550	7590	02/18/2010		
LSI Corporation c/o Suiter Swantz pc Ilo 14301 FNB Parkway, Suite 220 Omaha, NE 68154			EXAMINER	KIM, SU C
			ART UNIT	PAPER NUMBER
			2823	
		MAIL DATE	DELIVERY MODE	
		02/18/2010	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

***Continuation Sheet (PTO-303)***

***Response to Argument***

With respect to claims rejection under 35 U.S.C. 103(a), applicant argues that the claim rejected under U.S.C 103 (a) include "element that have not been disclosed , taught or suggested by any of the reference cited by the Paten Office, either alone or in combination" and also applicant argues that " independent claims 1, 9 and 17 each generally recite: interconnect between said plurality of platform array units being pre-routed on chip"

In response to applicant's contention, it is respectfully submitted that **Voogel** discloses all the claimed limitation including "interconnect between said plurality of platform array units being pre-routed on chip" below.

**Voogel** appears to show, see Fig. 5(B) & 6, interconnect 322(1), 320(1), 323(1) & 400(A) (Fig. 5) between said plurality of platform array units being pre-routed on chip (Fig. 6). Furthermore, "pre-rout" has no special meaning in the claims and all the chip has some forms of pre-rout wire or connection in a chip

Therefore, the rejection of claims 1-4, 8-12, 16, 17, and 24 under 35 U.S.C. 103(a) is deemed proper and the *prima facie* case of obviousness has been met and the rejection under 35 U.S.C. § 103 is deemed proper.

***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to SU C. KIM whose telephone number is (571)272-5972. The examiner can normally be reached on Monday - Friday, 10:00AM to 6:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew S. Smith can be reached on (571) 272-1907. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/SU C KIM/  
Examiner  
Art Unit 2823

/W. David Coleman/  
Primary Examiner, Art Unit 2823